

# Troubleshooting guide

<b>REVISION HISTORY</b>
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This file is a troubleshooting guide about various known issues regarding Xenomai.

The latest version is available at [this address](#).

For questions, corrections and improvements, write to [the mailing list](#).

## 1 Kernel configuration

When configuring the Linux kernel, some options should be avoided.

### CONFIG\_CPU\_FREQ

This allows the CPU frequency to be modulated with workload, but many CPUs change the TSC counting frequency also, which makes it useless for accurate timing when the CPU clock can change. Also some CPUs can take several milliseconds to ramp up to full speed.

### CONFIG\_CPU\_IDLE

Allows the CPU to enter deep sleep states, increasing the time it takes to get out of these sleep states, hence the latency of an idle system. Also, on some CPU, entering these deep sleep states causes the timers used by Xenomai to stop functioning.

### CONFIG\_CC\_STACKPROTECTOR

This option must be disabled on all platforms except x86\_64: it requires changes to the context switches currently only implemented for x86\_64.

### CONFIG\_KGDB

This option can not be enabled with current versions of the I-pipe patch.

For x86 specific options see also [this page](#).

## 2 Xenomai or I-pipe error in the kernel log

If the Xenomai and I-pipe messages do not appear in the kernel log as:

```
I-pipe: head domain Xenomai registered.
Xenomai: hal/<arch> started.
Xenomai: scheduling class idle registered.
Xenomai: scheduling class rt registered.
Xenomai: real-time nucleus v2.6.1 (Light Years Away) loaded.
Xenomai: debug mode enabled.
Xenomai: starting native API services.
Xenomai: starting POSIX services.
Xenomai: starting RTDM services.
```

Where <arch> is the architecture you use, check the following sections, they describe the usual error messages you may encounter.

### 2.1 Kernel stops after "Uncompressing Linux. . . done, booting the kernel."

This means that the kernel crashes before the console is enabled. You should enable the CONFIG\_EARLY\_PRINTK option. For some architectures (blackfin, x86, arm), enabling this option also requires passing the earlyprintk parameter on the kernel command line. See *Documentation/kernel-parameters.txt* for possible values.

For the ARM architecture, you have to enable CONFIG\_DEBUG\_KERNEL and CONFIG\_DEBUG\_LL in order to be able to enable CONFIG\_EARLY\_PRINTK.

### 2.2 Kernel stops with an OOPS

Please make sure that you have followed the ["Kernel configuration"](#) section. Then, try capturing the oops text (using a serial console or netconsole) post the oops to the [xenomai mailing list](#), with the kernel configuration you used to compile the failing kernel.

## 2.3 Kernel boots but does not print any message

Your distribution may be configured to pass the `quiet` option on the kernel command line. In this case, the kernel does not print all the log messages, however, they are still available using the `dmesg` command.

## 2.4 Xenomai: compiled for TSC, but CPU has no TSC

You selected a CPU which as a TSC ("Pentium classic", and above), but the CPU on which you run the kernel has no TSC. This issue was resolved in the I-pipe core for Linux 3.4, but for prior versions, you need to select a CPU without a TSC when configuring the kernel, and recompile the kernel.

## 2.5 Xenomai has detected a CPU frequency of 0

This happens when running in emulators. In such a case, Xenomai can not run as it needs to know the clock frequency.

This may also happen when using I-pipe core patch for Linux 3.2, compiled for a CPU with a TSC, but running on a CPU without a TSC, as in the "[Xenomai: compiled for TSC](#)" case.

## 2.6 I-pipe: could not find timer for cpu #x

See [code -19](#).

## 2.7 Xenomai: Local APIC absent or disabled!

See [code -19](#).

## 2.8 Xenomai: SMI-enabled chipset found, but SMI workaround disabled

First you should run the latency test under some load and see if you experience any pathological latency ("pathological" meaning more than, say, 100 micro-seconds). If you do not observe any such latency, then this warning is harmless, and if you find it annoying, you may disable "SMI detection" in Xenomai's configuration menu. You can skip the rest of this section.

If you observe any high latency then you have a problem with SMI, and this warning was intended for you. But the Xenomai configuration menu allow you to enable two workarounds which may help you. These workarounds may be found in the Machine/SMI workaround sub-menu of Xenomai configuration menu.

The first workaround which you should try is to disable all SMI sources. In order to do this, in the Xenomai configuration menu, select the options "Enable SMI workaround" and "Globally disable SMI". This option is the safest workaround, because when enabled, no SMI can interfere with hardware interrupt management behind your back and cause high latencies. Once this workaround enabled, you should run the latency test again, verify that your high latency disappeared but most importantly, verify that every peripheral you intend to use with Xenomai is working properly. If everything is working properly, then you are done with SMI.

If some peripheral is not working properly, then it probably needs SMI, in which case you can not simply disable SMI globally, you will need to disable all SMI sources on your system except the SMI needed by your peripheral. This is a much less safe choice, since Xenomai has to know all SMI sources to disable them, one by one. In order to choose this second workaround, unselect the option "Globally disable SMI", and select the option corresponding to your peripheral. For example, if you need legacy USB emulation to get your USB mouse working, select the option "Enable legacy USB emulation". You should then run the latency test again and verify that you do not observe any high latency and that all your peripherals are functioning correctly. If you can not find your peripheral in the list proposed in the Xenomai configuration menu, drop a mail to the [Xenomai mailing list](#), we will try and possibly add the proper option if needed. If when running the latency test again, your peripheral is working properly and you still observe high latencies, then you are out of luck, the peripheral you want is likely to be the cause of such latencies.

**Important**

On some systems, SMI may be involved in thermal throttling of the CPU. Thus, switching it off **can cause hardware damage** in overheat situations. Do not disable SMIs if you are in this case.

---

## 2.9 Xenomai: system init failed, code -19

The most probable reason is that Xenomai could not find a timer.

Check that you have not enabled one of the options in the "[Kernel configuration](#)" section.

### 2.9.1 On x86

You will most likely also see the following message:

```
Xenomai: Local APIC absent or disabled!  
Disable APIC support or pass "lapic" as bootparam.
```

Xenomai sends this message if the kernel configuration Xenomai was compiled against enables the local APIC support (`CONFIG_X86_I`) but the processor status gathered at boot time by the kernel says that no local APIC support is available. There are two options for fixing this issue:

- either your CPU really has *no* local APIC hw, then you need to rebuild a kernel with LAPIC support disabled, before rebuilding Xenomai against the latter;
- or it does have a local APIC but the kernel boot parameters did not specify to activate it using the "lapic" option. The latter is required since 2.6.9-rc4 for boxen which APIC hardware is disabled by default by the BIOS. You may want to look at the file *Documentation/kernel-parameters.txt* from the Linux source tree, for more information about this parameter.

### 2.9.2 On AMD x86\_64

You will most likely also see the following message:

```
I-pipe: cannot use LAPIC as a tick device  
I-pipe: disable C1E power state in your BIOS
```

Xenomai sends this message if C1E option is enabled in a BIOS. To fix this issue please disable C1E support in the BIOS. In some Award BIOS this option is located in the Advanced BIOS Features→ menu (AMD C1E Support).

**Important**

Disabling AMD K8 Cool&Quiet option in a BIOS does not solve the problem.

---

### 2.9.3 On other supported platforms

As on x86, on other platforms where Xenomai shares the timer with Linux, the timer is only used if it was not shut down by Linux. So you should check the log for messages about disabled timers. You can also check `/proc/timer_list` to see which timers are enabled. For instance, Xenomai on SMP systems requires per-cpu local timers, so the local timers should be enabled. In case of doubt, post a message to [the xenomai mailing list](#), sending:

- your kernel configuration
  - the contents of `/proc/timer_list` run on the exact kernel which has the issue
  - the complete kernel boot log.
-

### 2.9.4 On a new I-pipe port

You will most likely also see the following message:

```
I-pipe: could not find timer for cpu #x
```

Starting with the I-pipe patch for Linux 3.2, the timers provided by the I-pipe patch to Xenomai are registered at run-time. So, you may lack a `struct ipipe_timer` definition, and its registration with `ipipe_timer_register()` or with the `ipipe_timer` member of the `struct clock_event_device` structure.

For an example on the ARM platform see [this page](#).

### 2.10 Xenomai: system init failed, code -22

On the ppc64 platform, check whether `CONFIG_PPC_64K_PAGES` is defined in your kernel configuration. If so, then you likely need to raise all Xenomai parameters defining the size of internal heaps, such as `CONFIG_XENO_OPT_SYS_HEAPSZ`, `CONFIG_XENO_OPT_GLOBAL_SEM_HEAPSZ`, `CONFIG_XENO_OPT_SEM_HEAPSZ` and `CONFIG_XENO_OPT_SYS_STACKPO` so that  $(\text{size} / 64\text{k}) > 2$ . The default values for these parameters are currently based on the assumption that `PAGE_SIZE = 4k`.

## 3 Problems when running the latency test

The first test to run to see if Xenomai is running correctly on your platform is the latency test. The following sections describe the usual reasons for this test not to run correctly.

### 3.1 Xenomai: binding failed: Operation not permitted

This error message means that you are trying to run the latency test as a non-root user. Using Xenomai services requires root privileges (more precisely `CAP_SYS_NICE`). However, you can allow a specific group to access Xenomai services, by following the instructions on [this page](#).

### 3.2 Xenomai: --enable-x86-sep needs NPTL and Linux 2.6.x or higher

On the x86 architecture, the configure script option `--enable-x86-sep` allows Xenomai to use the `SYSENTER/SYSEXIT` mechanism for issuing system calls.

However, this mechanism requires support from the `libc`. Currently, we know the `glibc` with NPTL has this support, other libraries will cause Xenomai applications to fail with this error message.

### 3.3 latency: failed to open benchmark device

You have launched `latency -t 1` or `latency -t 2` which both require the kernel to have been compiled with the `CONFIG_XENO_DRIVERS_TIMERBENCH` option enabled.

### 3.4 Hardware tsc is not a fast wrapping one

See the "[ARM tsc emulation issues](#)" section.

### 3.5 Xenomai: incompatible ABI revision level

Each Xenomai branch (2.1, 2.2, 2.3, 2.4, 2.5, 2.6, ...) defines a kernel/user ABI, so that it is possible to mix kernels and user-space supports of different versions in the same branch. So, for instance, after having build a system with a kernel and user-space support using Xenomai 2.6.0, it is possible to update the user-space support to Xenomai 2.6.1 without changing the kernel.

However, it is not possible to mix kernel and user-space supports of different branches.

A common reason for this error is when you run a kernel compiled with Xenomai 2.6.1 support on a system where you have a user-space installed by your Debian based Linux distribution (notably Ubuntu) from the 2.5 branch, this can not work, the two branches use different ABIs. See [README.INSTALL](#) for details on how to compile a user-space support, or to build a new `xenomai-runtime` Debian package.

If you compiled and installed the correct Xenomai user-space support, there are probably files on your system remaining from a previous installation.

### 3.6 Xenomai: incompatible feature set

Since kernel-space support and user-space support are compiled separately, each Xenomai application checks, at startup, whether the kernel and user-space supports have been configured with compatible options. If you see this message, it means they have not. See [README.INSTALL](#) for further details. The following sections detail the most frequent reasons for this message.

#### 3.6.1 missing="kuser\_tsc"

See the "[ARM tsc emulation issues](#)" section.

#### 3.6.2 missing="sep"

On the x86 architecture, the configure script option `--enable-x86-sep` allows Xenomai to use the SYSENTER/SYSEXIT mechanism for issuing system calls.

However, this mechanism requires a recent kernel (2.6 or higher).

#### 3.6.3 missing="smp/nosmp"

On some SMP-capable architectures, for kernel-space and user-space supports to be compatible, both should be compiled with the same setting for SMP.

SMP support in kernel-space is enabled with the `CONFIG_SMP` option.

For these architectures, SMP support in user-space is enabled by passing `--enable-smp` to the configure script, and disabled by passing `--disable-smp` (SMP is enabled by default on some platforms).

Other SMP-capable architectures may run userland code built with `--enable-smp` or `--disable-smp` over the same kernel indifferently, at no noticeable performance cost. These architectures never receive such SMP-related error.

#### 3.6.4 missing="tsc"

This error is specific to the x86 architecture. You enabled tsc in user-space by passing the `--enable-x86-tsc` option, but you selected a processor when configuring the kernel which has no tsc.

So, if your processor has a tsc (all Intel processors starting with some Pentium and Pentium Pro have a tsc), you probably mis-configured your kernel and should select the exact processor you are using in the kernel configuration and recompile it.

If your processor does not have a tsc, you should not pass the `--enable-x86-tsc` option to the configure script.

### 3.7 Xenomai: kernel/user tsc emulation mismatch

See the "[ARM tsc emulation issues](#)" section.

### 3.8 Xenomai: native skin or CONFIG\_XENO\_OPT\_PERVASIVE disabled

Possible reasons for this error are:

- you booted a kernel without Xenomai or I-pipe support, a kernel with I-pipe and Xenomai support should have a `/proc/ip-pipe/version` and `/proc/xenomai/version` files;
- the kernel you booted does not have the `CONFIG_XENO_SKIN_NATIVE` and `CONFIG_XENO_OPT_PERVASIVE` options enabled;
- Xenomai failed to start, check the "[Xenomai or I-pipe error in the kernel log](#)" section;
- you are trying to run Xenomai user-space support compiled for `x86_32` on an `x86_64` kernel.

### 3.9 latency: not found

On the ARM platform this message happens when there is a mismatch between kernel and user for the EABI setting: for instance you compiled the user-space support with a toolchain generating OABI code, and are trying to run the result on a kernel with `CONFIG_AEABI` but without `CONFIG_OABI_COMPAT`. Or vice versa, when running user-space compiled with an EABI toolchain on a kernel without `CONFIG_AEABI`.

### 3.10 Xenomai: watchdog triggered (period too short?)

Xenomai watchdog has stopped the latency test because it was using all the CPU in primary mode. This is likely due to a too short period, re-run the latency test passing a longer period using the `-p` option.

### 3.11 Xenomai: Your board/configuration does not allow tsc emulation

See the "[ARM tsc emulation issues](#)" section.

### 3.12 the latency test hangs

The most common reason for this issues is a too short period passed with the `-p` option, try increasing the period. If you enable the watchdog (option `CONFIG_XENO_OPT_WATCHDOG`, in your kernel configuration), you should see the "[Xenomai: watchdog triggered \(period too short?\)](#)" message.

### 3.13 the latency test shows high latencies

The latency test runs, but you are seeing high latencies.

- make sure that you carefully followed the "[Kernel configuration](#)" section.
- make sure that you do not have an issue with SMIs, see the [section about SMIs](#). Note that if you have an Intel chipset and you do not see the message:

```
Xenomai: SMI-enabled chipset found, but SMI workaround disabled
```

in the boot logs, it may mean that your chipset is not detected.

- if you have some legacy USB switch at BIOS configuration level, try disabling it.

- if you do not have this option at BIOS configuration level, it does not necessarily mean that there is no support for it, thus no potential for high latencies; this support might just be forcibly enabled at boot time. To solve this, in case your machine has some USB controller hardware, make sure to enable the corresponding host controller driver support in your kernel configuration. For instance, UHCI-compliant hardware needs `CONFIG_USB_UHCI_HCD`. As part of its init chores, the driver should reset the host controller properly, kicking out the BIOS off the concerned hardware, and deactivate the USB legacy mode if set in the same move.
- if you observe high latencies while running X-window, try disabling hardware acceleration in the X-window server file; in the `Device` section of `/etc/X11/XF86Config-4` add the following line:

```
Option "NoAccel"
```

### 3.14 ARM tsc emulation issues

In order to allow applications to measure short durations with as little overhead as possible, Xenomai uses a 64 bits high resolution counter. On x86, the counter used for this purpose is the time-stamp counter, aka "tsc".

ARM processors generally do not have a 64 bits high resolution counter available in user-space, so this counter is emulated by reading whatever high resolution counter is available on the processor, and used as clock source in kernel-space, and extend it to 64 bits by using data shared with the kernel. If Xenomai libraries are compiled without emulated tsc support, system calls are used, which have a much higher overhead than the emulated tsc code.

In recent versions of the I-pipe patch, SOCs generally select the `CONFIG_IPIPE_ARM_KUSER_TSC` option, which means that the code for reading this counter is provided by the kernel at a predetermined address (in the vector page, a page which is mapped at the same address in every process) and is the code used if you do not pass the `--enable-arm-tsc` or `--disable-arm-tsc` option to configure, or pass `--enable-arm-tsc=kuser`.

This default should be fine with recent patches and most ARM SOCs.

However, if you see the following message:

```
Xenomai: incompatible feature set
(userland requires "kuser_tsc...", kernel provides..., missing="kuser_tsc")
```

It means that you are either using an old patch, or that the SOC you are using does not select the `CONFIG_IPIPE_ARM_KUSER_TSC` option (to this date the only in-tree SOC family not using this option is `ixp4xx`).

So you should resort to what Xenomai did before branch 2.6: select the tsc emulation code when compiling Xenomai user-space support by using the `--enable-arm-tsc` option. The parameter passed to this option is the name of the SOC or SOC family for which you are compiling Xenomai. Typing:

```
/patch/to/xenomai/configure --help
```

will return the list of valid values for this option.

If after having enabled this option and recompiled, you see the following message when starting the latency test:

```
Xenomai: kernel/user tsc emulation mismatch
```

or

```
Hardware tsc is not a fast wrapping one
```

It means that you selected the wrong SOC or SOC family, reconfigure Xenomai user-space support by passing the right parameter to `--enable-arm-tsc` and recompile.

The following message:

```
Xenomai: Your board/configuration does not allow tsc emulation
```

means that the kernel-space support for the SOC you are using does not provide support for tsc emulation in user-space. In that case, you should recompile Xenomai user-space support passing the `--disable-arm-tsc` option.

## 4 switchtest fails with "pthread\_create: Resource temporarily unavailable"

The switchtest test creates many kernel threads, this means that the options `CONFIG_XENO_OPT_SYS_HEAPSZ` and `CONFIG_XENO_` in your kernel configuration, should be configured to large enough values. Try increasing them and recompiling the kernel.

## 5 Known Bugs and Limitations

### 5.1 2.6.2/x86

2.6.2 (like any previous Xenomai release) does not handle the extended processor state (`xsave/xrstor`) yet.

2.6.2 automatically disables this CPU feature at boot when the host kernel detects it, so no action is to be taken by the user. However, this feature shall be disabled manually for older Xenomai releases, by passing the "noxsave" parameter on the kernel command line (see `Documentation/kernel-parameters.txt`).

Failing to do so, running with extended processor state support enabled on these Xenomai releases beget random execution errors in userland, typically when the switchtest program runs in the background, due to incorrect FPU management in real-time mode.

## 6 Problem with my code (not Xenomai code)

### 6.1 "Warning: <service> is deprecated" while compiling kernel code

Where <service> is a thread creation service, one of:

- `cre_tsk`
- `pthread_create`
- `rt_task_create`
- `sc_tcreate` or `sc_tcreate`
- `taskSpawn` or `taskInit`
- `t_create`

Starting with Xenomai 3, the skins will not export their interface to kernel modules anymore, at the notable exception of the RTDM device driver API, which by essence must be used from kernel space for writing real-time device drivers. Those warnings are there to remind you that application code should run in user-space context instead.

The reason for this is fully explained in the project Roadmap document, see ["What Will Change With Xenomai 3"](#).

You may switch those warnings off by enabling the `CONFIG_XENO_OPT_NOWARN_DEPRECATED` option in your kernel configuration, but nevertheless, you have been **WARNED**.

### 6.2 "Xenomai: process memory not locked (missing mlockall?)" at startup

In order to avoid unwanted transitions to secondary domain, an application using Xenomai services should call, before any Xenomai service:

```
mlockall(MCL_CURRENT | MCL_FUTURE);
```

Even if your system has no swap, the Linux kernel may "swap out" some pages, for instance the program pages which are known to exist on flash or on disk, causing page faults (and a secondary mode switch for xenomai threads running in primary mode) when the program tries to access this page.

So, Xenomai libraries abort when an application which has not called `mlockall` is detected.

Note that some skins allow `mlockall` to be called automatically by Xenomai libraries startup, this is enabled when configuring Xenomai user-space support with the `configure` script.

See `configure --help`.

### 6.3 High latencies when transitioning from primary to secondary mode

Such transition requires to wake up the Linux task underlying your real-time thread when running in secondary mode, since the latter needs to leave the Xenomai domain for executing under the control of the regular Linux scheduler. Therefore, it all depends on the Linux kernel granularity, i.e. its ability to reach the next rescheduling point as soon as such wakeup has been requested. Additionally, the task wakeup request is performed from a virtual interrupt handler which has to be run from the Linux domain upon request from the Xenomai domain, so the time required to handle and dispatch this interrupt outside of any critical kernel section also needs to be accounted for. Even if the kernel granularity improves at each new release, there are still a few catches:

- Although the use of DMA might induce additional interrupt latency due to bus bandwidth saturation, disabling it for disk I/O is a bad idea when using mixed real-time modes. This is due to the fact that using PIO often leads to lengthy non-preemptible sections of kernel code being run from e.g. IDE drivers, from which pending real-time mode transitions could be delayed. In the same vein, make sure that your IDE driver runs in unmasked IRQ mode. In any case, a quick check using the "hdparm" tool will help:

```
# hdparm -v /dev/hda

/dev/hda:
...
unmaskirq    = 1 (on)
using_dma    = 1 (on)
...
```

- Even if your application does not directly request disk I/O, remember that the kernel routinely performs housekeeping duties which do, like filesystem journal updates or VM commits to the backing store, so latencies due to improper disk settings may well trigger apparently randomly. Of course, if your application only operates in primary mode during all of its time critical duties, i.e. never request Linux syscalls, it will not be adversely affected by DMA deactivation or IDE masking, since it will remain in the Xenomai domain, and activities from such domain can preempt any activity from the Linux domain, including disk drivers.

### 6.4 Any Xenomai service fails with code -38 (ENOSYS)

Possible reasons for this error are:

- you booted a kernel without Xenomai or I-pipe support, a kernel with I-pipe and Xenomai support should have a `/proc/i-pipe/version` and `/proc/xenomai/version` files;
- the kernel you booted does not have the `CONFIG_XENO_SKIN_*` option enabled for the skin you use, or `CONFIG_XENO_OPT_PER` is disabled;
- Xenomai failed to start, check the "[Xenomai or I-pipe error in the kernel log](#)" section;
- you are trying to run Xenomai user-space support compiled for `x86_32` on an `x86_64` kernel.

## 6.5 My application reserves a lot of memory

Your user-space application unexpectedly reserves a lot of virtual memory, as reported by `top` or `/proc/<pid>/maps`. Sometimes OOM situations even appear during runtime on systems with limited memory.

The Xenomai tasks are underlaid by native POSIX threads, for which a huge default amount of stack space memory is reserved by the native POSIX support, usually 8MiB per thread, so the overall allocated space is about  $8\text{MiB} * \text{nr\_threads}$ , which are likely to be locked using the `mlockall()` service, which in turn even commits such space to RAM.

Unfortunately, this behaviour cannot be controlled by the `stacksize` parameter passed to the various thread creation routines, i.e. the latter is about limiting the addressable stack space on a per-thread basis, but does not affect the amount of stack memory initially reserved by the POSIX library. A work-around consists of setting a lower user-limit for initial stack allocation, like calling:

```
ulimit -s <initial-size-in-kbytes>
```

in your parent shell before running your application (defaults to 8192).